

FIG. 1

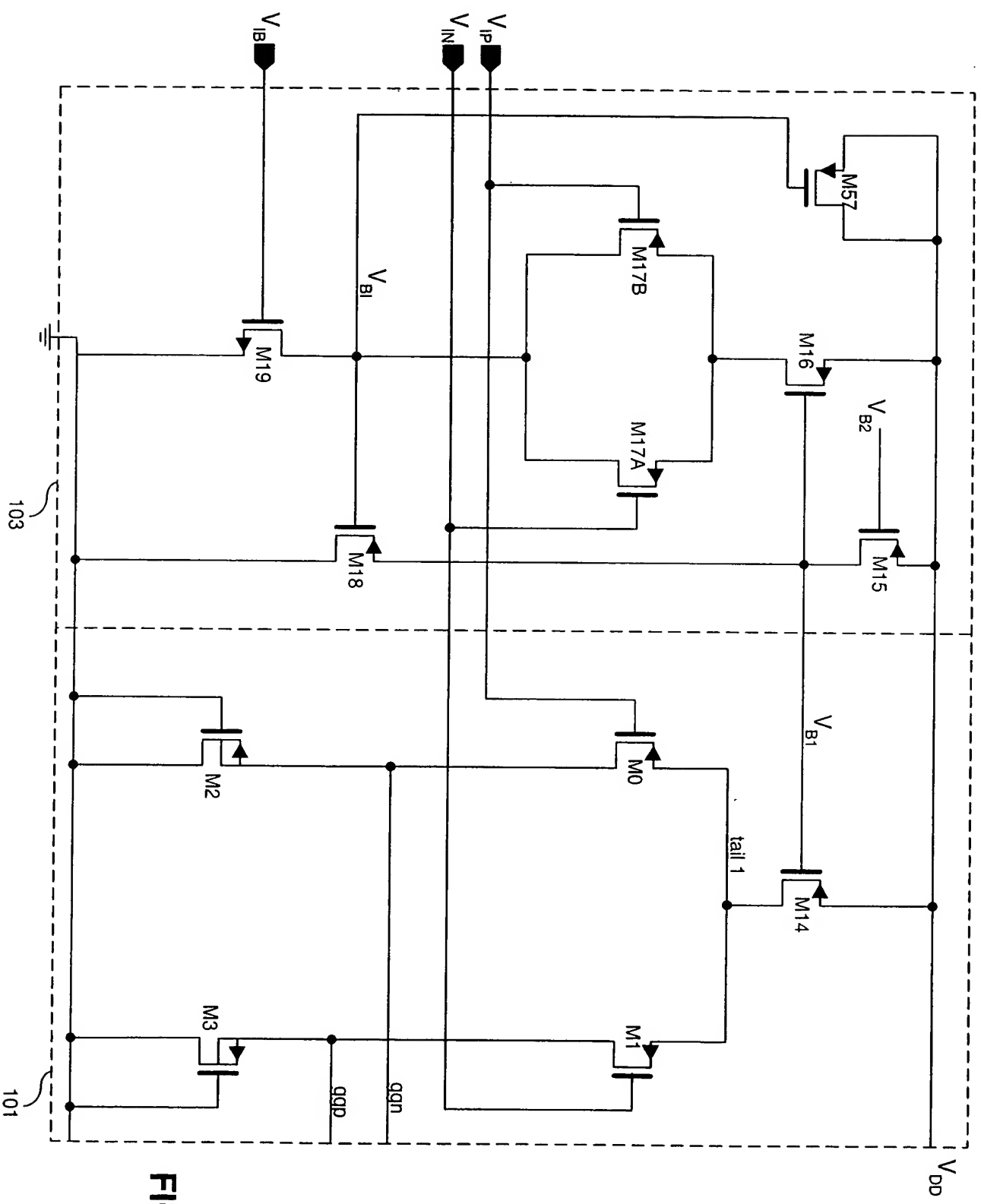


FIG. 2

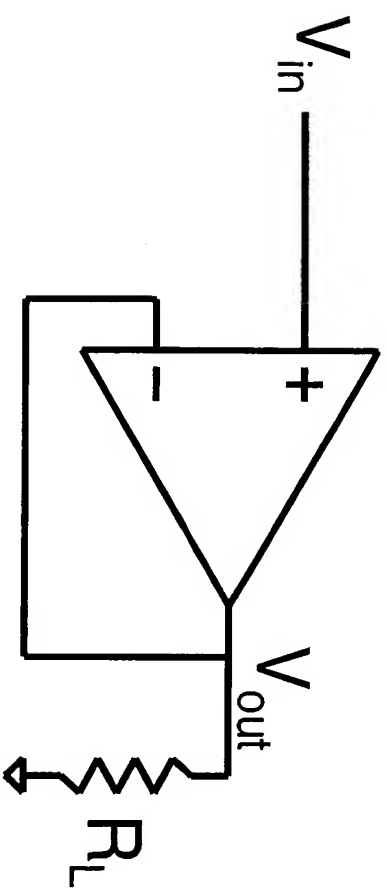


FIG. 3

Normalized Worst-Case Input Stage Gain over Process and Temp ($V_{DD} = 2.5V$)

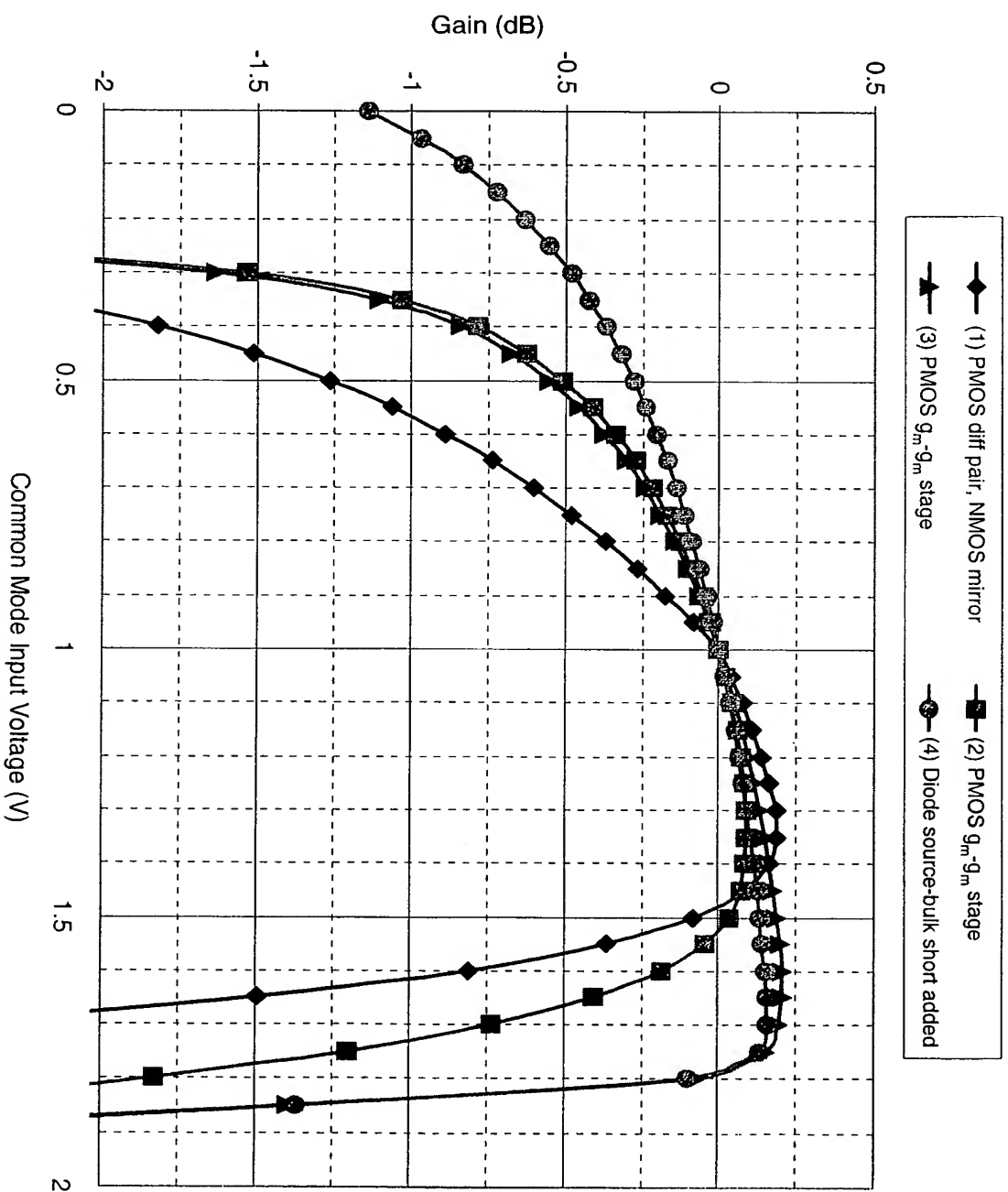


FIG. 4